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#### SNOSBQ4D - MAY 1999 - REVISED MARCH 2013

# LM567/LM567C Tone Decoder

Check for Samples: LM567, LM567C

### **FEATURES**

- 20 to 1 Frequency Range with an External Resistor
- Logic Compatible Output with 100 mA Current Sinking Capability
- Bandwidth Adjustable from 0 to 14% •
- High Rejection of Out of Band Signals and • Noise
- Immunity to False Signals •
- **Highly Stable Center Frequency**
- Center Frequency Adjustable from 0.01 Hz to • 500 kHz

#### **APPLICATIONS**

- **Touch Tone Decoding** •
- **Precision Oscillator**
- **Frequency Monitoring and Control** •
- Wide Band FSK Demodulation
- **Ultrasonic Controls**
- **Carrier Current Remote Controls** •
- **Communications Paging Decoders**

#### DESCRIPTION

The LM567 and LM567C are general purpose tone decoders designed to provide a saturated transistor switch to ground when an input signal is present within the passband. The circuit consists of an I and Q detector driven by a voltage controlled oscillator which determines the center frequency of the External components are used to decoder. independently set center frequency, bandwidth and output delay.

### **CONNECTION DIAGRAM**



Figure 1. Metal Can Package **Top View** See Package Number LMC0008C OBSOLETE



Figure 2. PDIP and SOIC Packages Top View See Package Number D0008A See Package Number P0008E



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#### SNOSBQ4D-MAY 1999-REVISED MARCH 2013

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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### ABSOLUTE MAXIMUM RATINGS<sup>(1)(2)(3)</sup>

Supply Voltage Pin	9V
Power Dissipation <sup>(4)</sup>	1100 mW
V <sub>8</sub>	15V
V <sub>3</sub>	-10V
V <sub>3</sub>	V <sub>4</sub> + 0.5V
Storage Temperature Range	−65°C to +150°C
Operating Temperature Range	
LM567H	−55°C to +125°C
LM567CH, LM567CM, LM567CN	0°C to +70°C
Soldering Information	
PDIP Package	
Soldering (10 sec.)	260°C
SOIC Package	
Vapor Phase (60 sec.)	215°C
Infrared (15 sec.)	220°C
See http://www.ti.com for other methods of coldering surface mount devices	

See http://www.ti.com for other methods of soldering surface mount devices.

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions which ensure specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not ensured for parameters where no limit is given, however, the typical value is a good indication of device performance.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

(3) Refer to RETS567X drawing for specifications of military LM567H version.

(4) The maximum junction temperature of the LM567 and LM567C is 150°C. For operating at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of 150°C/W, junction to ambient or 45°C/W, junction to case. For the DIP the device must be derated based on a thermal resistance of 110°C/W, junction to ambient. For the SOIC package, the device must be derated based on a thermal resistance of 160°C/W, junction to ambient.

## ELECTRICAL CHARACTERISTICS

AC Test Circuit,  $T_A = 25^{\circ}C$ ,  $V^+ = 5V$ 

Perometero	Conditiono		LM567		LM	Unite		
Farameters	Conditions	Min	Min Typ Max		Min	Тур	Max	Units
Power Supply Voltage Range		4.75	5.0	9.0	4.75	5.0	9.0	V
Power Supply Current Quiescent	$R_L = 20k$		6	8		7	10	mA
Power Supply Current Activated	$R_L = 20k$		11	13		12	15	mA
Input Resistance		18	20		15	20		kΩ
Smallest Detectable Input Voltage	$I_{L} = 100 \text{ mA}, f_{i} = f_{o}$		20	25		20	25	mVrms
Largest No Output Input Voltage	$I_{C} = 100 \text{ mA}, f_{i} = f_{o}$	10	15		10	15		mVrms
Largest Simultaneous Outband Signal to Inband Signal Ratio			6			6		dB
Minimum Input Signal to Wideband Noise Ratio	B <sub>n</sub> = 140 kHz		-6			-6		dB
Largest Detection Bandwidth		12	14	16	10	14	18	% of $f_o$
Largest Detection Bandwidth Skew			1	2		2	3	% of $f_o$
Largest Detection Bandwidth Variation with Temperature			±0.1			±0.1		%/°C
Largest Detection Bandwidth Variation with Supply Voltage	4.75–6.75V		±1	±2		±1	±5	%V
Highest Center Frequency		100	500		100	500		kHz



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## **ELECTRICAL CHARACTERISTICS (continued)**

AC Test Circuit,  $T_A = 25^{\circ}C$ ,  $V^+ = 5V$ 

Barrana	O an all the ma		LM567		LM	Unito		
Parameters	Conditions	Min	Тур	Max	Min	Тур	Max	Units
Center Frequency Stability (4.75–5.75V)	0 < T <sub>A</sub> < 70 -55 < T <sub>A</sub> < +125		35 ± 60 35 ± 140			35 ± 60 35 ± 140		ppm/°C ppm/°C
Center Frequency Shift with Supply Voltage	4.75V–6.75V 4.75V–9V		0.5	1.0 2.0		0.4	2.0 2.0	%/V %/V
Fastest ON-OFF Cycling Rate			f <sub>o</sub> /20			f <sub>o</sub> /20		
Output Leakage Current	V <sub>8</sub> = 15V		0.01	25		0.01	25	μA
Output Saturation Voltage	$e_i = 25 \text{ mV}, I_8 = 30 \text{ mA}$ $e_i = 25 \text{ mV}, I_8 = 100 \text{ mA}$		0.2 0.6	0.4 1.0		0.2 0.6	0.4 1.0	V
Output Fall Time			30			30		ns
Output Rise Time			150			150		ns

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6

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R3

C1

Figure 13. Touch-Tone Decoder

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Connect Pin 3 to 2.8V to Invert Output









Figure 16. Precision Oscillator Drive 100 mA Loads



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### AC TEST CIRCUIT



 $f_i = 100 \text{ kHz} + 5\text{V}$ \***Note:** Adjust for  $f_o = 100 \text{ kHz}$ .

#### **APPLICATIONS INFORMATION**

The center frequency of the tone decoder is equal to the free running frequency of the VCO. This is given by

$$f_o \cong \frac{1}{1.1 R_1 C_1}$$

The bandwidth of the filter may be found from the approximation

BW = 1070 
$$\sqrt{\frac{V_i}{f_o C_2}}$$
 in % of  $f_o$ 

where

- V<sub>i</sub> = Input voltage (volts rms), V<sub>i</sub> ≤ 200mV
- $C_2$  = Capacitance at Pin 2(µF)

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### LM567C MDC MWC ToNE DECODER



Figure 17. Die Layout (C - Step)

Table 1. Die/Wafer	Characteristics
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Fabrication A	ttributes	General Die Information				
Physical Die Identification	LM567C	Bond Pad Opening Size (min)	91µm x 91µm			
Die Step	C	Bond Pad Metalization	0.5% COPPER_BAL. ALUMINUM			
Physical Att	ributes	Passivation	VOM NITRIDE			
Wafer Diameter	150mm	Back Side Metal	BARE BACK			
Dise Size (Drawn)	1600µm x 1626µm 63.0mils x 64.0mils	Back Side Connection	Floating			
Thickness	406µm Nominal					
Min Pitch	198µm Nominal					
Special Assembly Requirements:						
Note: Actual die size is rounded to the	ne nearest micron.					

		Die Bond Pad Co	oordinate Location	s (C - Step)		
	(Referenced to	die center, coordina	ates in µm) NC = No	Connection, N.U. =	Not Used	
		X/Y COOF	RDINATES		PAD SIZE	
SIGNAL NAME	PAD# NUMBER	X	Y	X		Y
OUTPUT FILTER	1	-673	686	91	x	91
LOOP FILTER	2	-673	-419	91	x	91
INPUT	3	-673	-686	91	x	91
V+	4	-356	-686	91	x	91
TIMING RES	5	673	-122	91	x	91
TIMING CAP	6	673	76	91	x	91
GND	7	178	686	117	x	91
OUTPUT	8	-318	679	117	x	104

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#### **REVISION HISTORY**

Cł	nanges from Revision C (March 2013) to Revision D P	'age	
•	Changed layout of National Data Sheet to TI format	. 10	

Product Folder Links: LM567 LM567C





### PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM567CM	ACTIVE	SOIC	D	8	95	TBD	Call TI	Call TI	0 to 70	LM 567CM	Samples
LM567CM/NOPB	ACTIVE	SOIC	D	8	95	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 70	LM 567CM	Samples
LM567CMX/NOPB	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	0 to 70	LM 567CM	Samples
LM567CN	LIFEBUY	PDIP	Р	8	40	TBD	Call TI	Call TI	0 to 70	LM 567CN	
LM567CN/NOPB	ACTIVE	PDIP	Р	8	40	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 70	LM 567CN	Samples
NE567V	LIFEBUY	PDIP	Р	8	40	TBD	Call TI	Call TI	0 to 70	LM 567CN	

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

<sup>(4)</sup> There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.



## PACKAGE OPTION ADDENDUM

12-Jul-2014

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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# PACKAGE MATERIALS INFORMATION

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## TAPE AND REEL INFORMATION





## QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal	
	-

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM567CMX/NOPB	SOIC	D	8	2500	330.0	12.4	6.5	5.4	2.0	8.0	12.0	Q1

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# PACKAGE MATERIALS INFORMATION

26-Mar-2013



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM567CMX/NOPB	SOIC	D	8	2500	367.0	367.0	35.0

P(R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE



- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001 variation BA.



D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AA.



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